

REMARKS

In the Office Action mailed June 16, 2005, Claims 1, 6, and 24-27 are rejected under 35 USC §102(b) as being anticipated by Johnson (U.S. Patent 4,947,366). Claims 2, 6, 12-16, 21, 22, and 29-32 are rejected under 35 USC §102(b) as being obvious over Johnson in view of Loyer et al. (U.S. Patent 6,298,396 "Loyer").

Rejection of Claims under 35 USC §102

In response to the rejection of Claims 1, 6, and 24-27 under 35 USC §102(b) as being anticipated by Johnson, Applicants have amended independent claim 1 to further include (i) configurable system logic having programmable logic, and (ii) a configurable system interconnect coupled between the configurable system logic and the DMA controller. Applicants have further amended claim 1 to recite an input/output (I/O) device coupled to the DMA controller by way of the configurable system interconnect, wherein the I/O device is implemented in the programmable logic. Applicants respectfully submit that neither reference discloses or suggests an input/output device implemented in programmable logic and coupled to a DMA controller. Applicants respectfully request reconsideration of claim 1 and its dependent claims 6 and 24-27 in view of the amendments to claim 1.

Rejection of Claims under 35 USC §103

In response to the rejection of claims 2, 6, 12-16, 21, 22, and 29-32 under 35 USC §102(b) as being obvious over Johnson in view of Loyer, Applicants have amended each of claims 12, 13 and 15 to distinguish over the combination of references. In particular, Applicants have amended independent claim 12 to include:

- (i) configurable system logic having programmable logic;
- (ii) a configurable system interconnect coupled between the configurable system logic and a DMA controller; and
- (iii) an input/output (I/O) device coupled to the DMA controller, wherein the I/O device is implemented in the programmable logic and the DMA controller re-executes a DMA transfer from the beginning with the I/O device upon receiving a retransmit request signal from the I/O device.

Applicants respectfully submit that neither reference discloses or suggest any of the elements (i)-(iii), and respectfully request reconsideration of independent claim 12 and its dependent claims 29, 30, 31 and 32.

Applicants have also amended independent claims 13 and 15 to include steps of:

- (i) configuring a first device in programmable logic of an integrated circuit;
- (ii) coupling the first device to a configurable system interconnect; and
- (iii) transferring data between the first device and a second device under control of a direct memory access (DMA) controller by way of the configurable system interconnect.

Applicants respectfully submit that nether reference discloses or suggests configuring a first device in programmable logic of an integrated circuit. Applicants further submit that neither reference discloses or suggest coupling the first device to a configurable system interconnect, or transferring data to or from such a device configured in programmable logic under control of a direct memory access (DMA) controller by way of the configurable system interconnect. Applicants respectfully request reconsideration of claims 13 and 15 and their dependent claims 14, 16 and 21-22 in view of the amendments to the independent claims.

CONCLUSION

In light of the above amendments and remarks, it is believed that claims 1-2, 6, 12-16, and 21-22, 24-27, and 29-32 are in condition for allowance and, therefore, a

Notice of Allowance of claims 1-2, 6, 12-16, and 21-22, 24-27, and 29-32 is respectfully requested. If the Examiner's next action is other than allowance as requested, the Examiner is requested to call the undersigned at (408) 879-6149.

Respectfully submitted,



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I hereby certify that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. BOX 1450, Alexandria, VA 22313-1450, on September 13, 2005.

Pat Slaback
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Signature